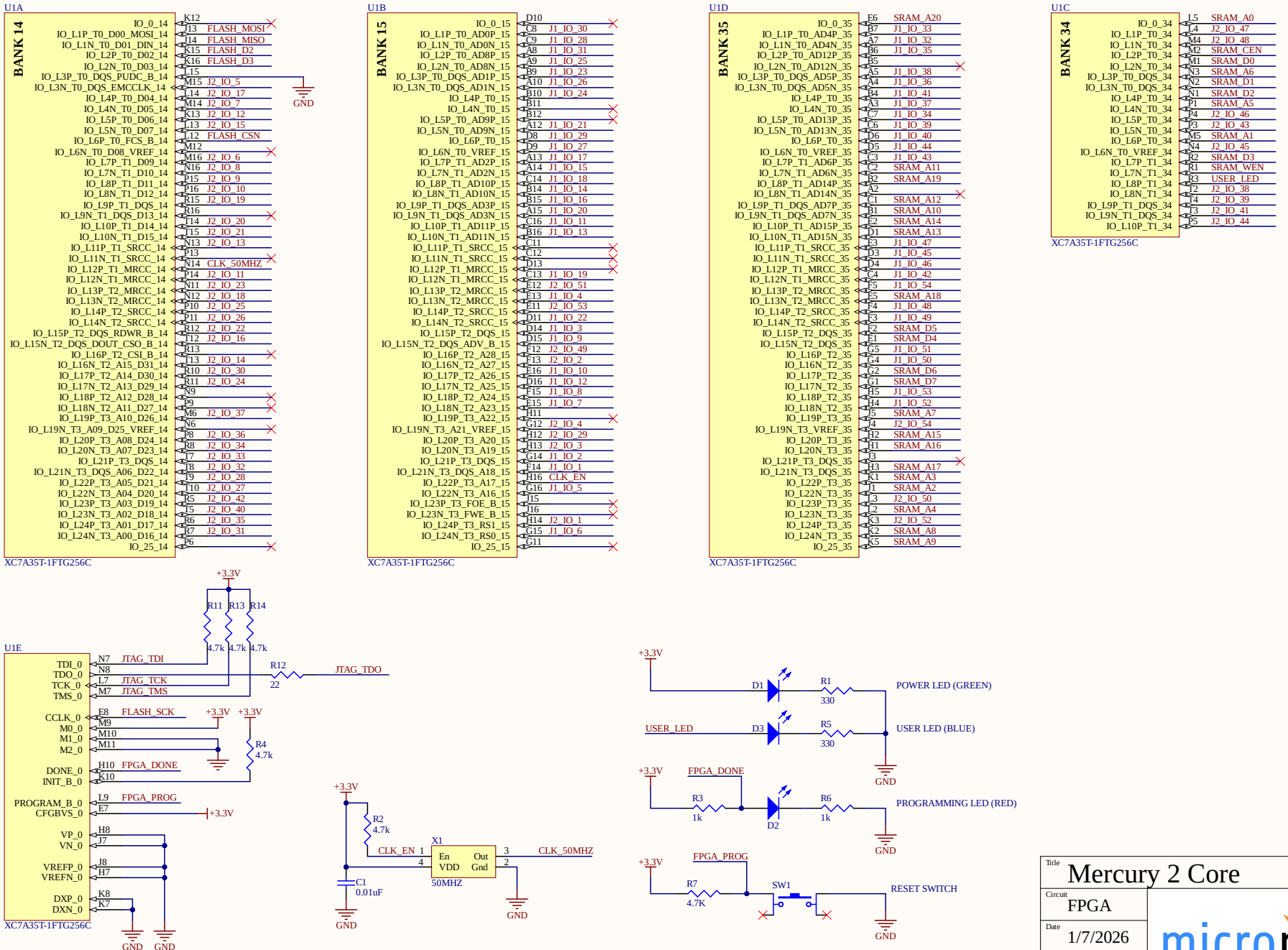
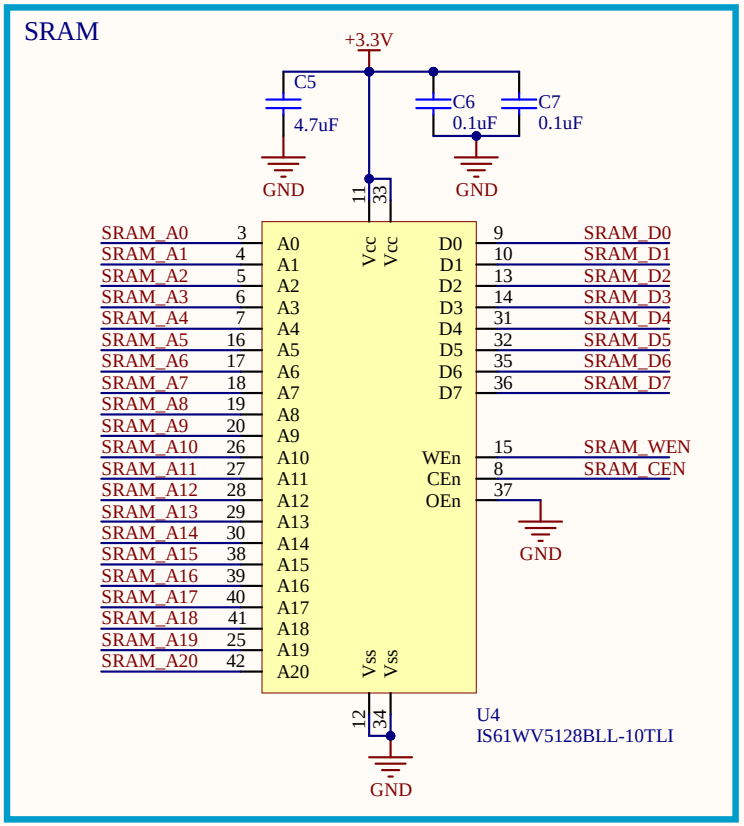
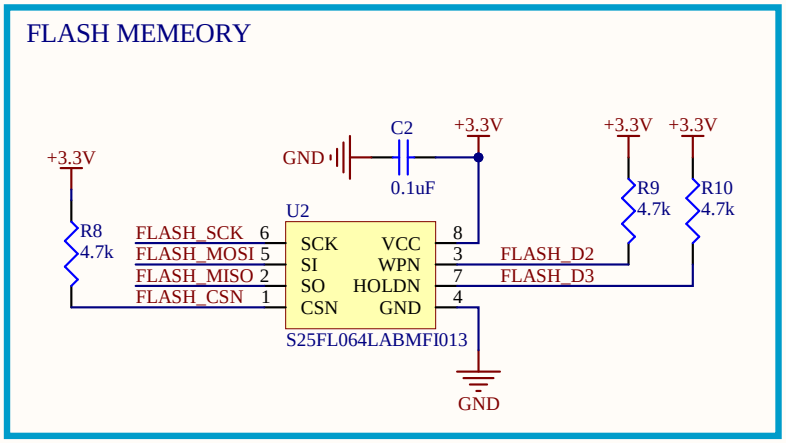


Title	Mercury 2 Core
Rev	1.A
Circuit	FPGA
Date	1/7/2026
Author	JV, ER
Sheet #	1 of 5

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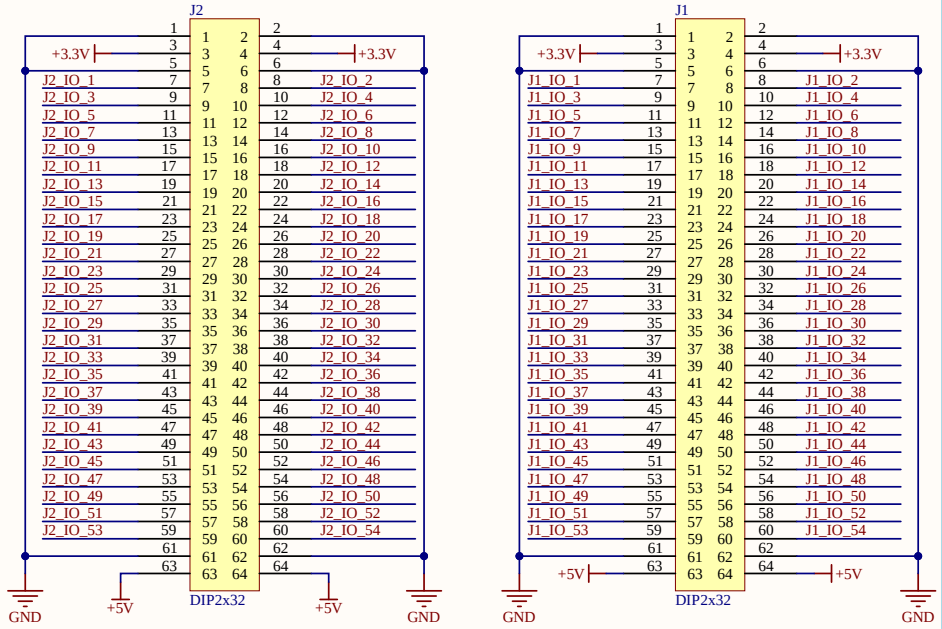


Title	Mercury 2 Core
Rev	1.A
Circuit	Memory
Date	1/7/2026
Author	JV, ER
Sheet #	2 of 5

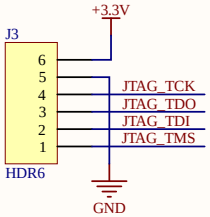
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GPIO



JTAG CONNECTOR

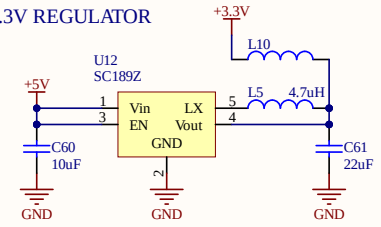


Title	Mercury 2 Core
Rev	1.A
Circuit	I/O
Date	1/7/2026
Author	JV, ER
Sheet #	3 of 5

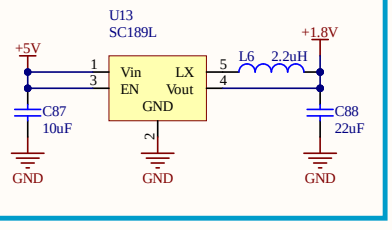
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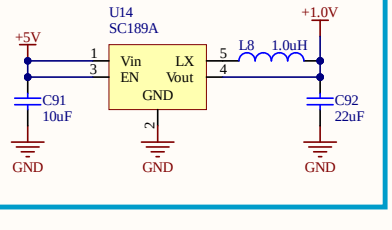
3.3V REGULATOR



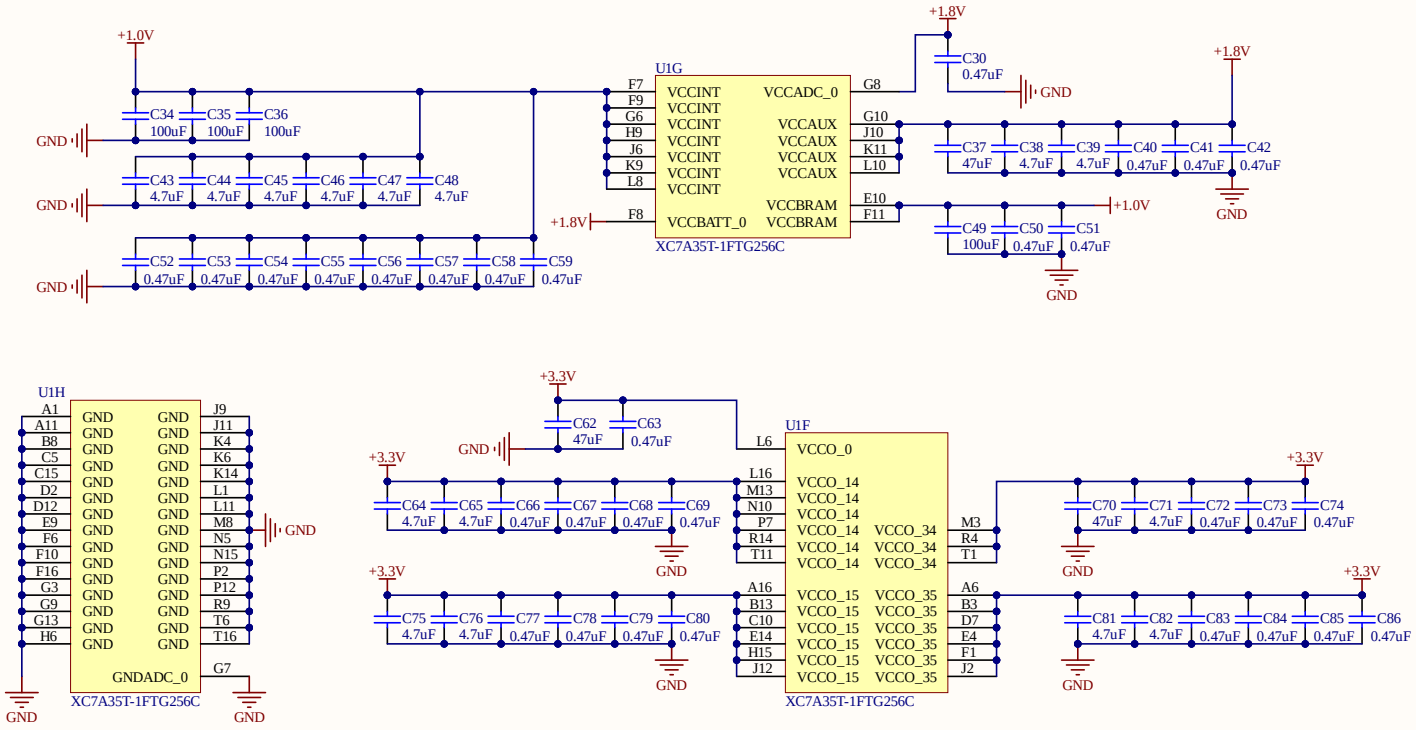
1.8V REGULATOR



1.0V REGULATOR



FPGA POWER



Title	Mercury 2 Core
Rev	1.A
Circuit	POWER
Date	1/7/2026
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